SPECIFICATION

Model No.: UMS-3005AN-YG

☐ **CUSTOMER :** _____

APPROVED BY: _____ **DATE:** _____

※Please Sign and return one copy

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PREPARED BY: _____ **DATE:** July 16 '96

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1. Basic Specifications

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1-1 Mechanical specifications:

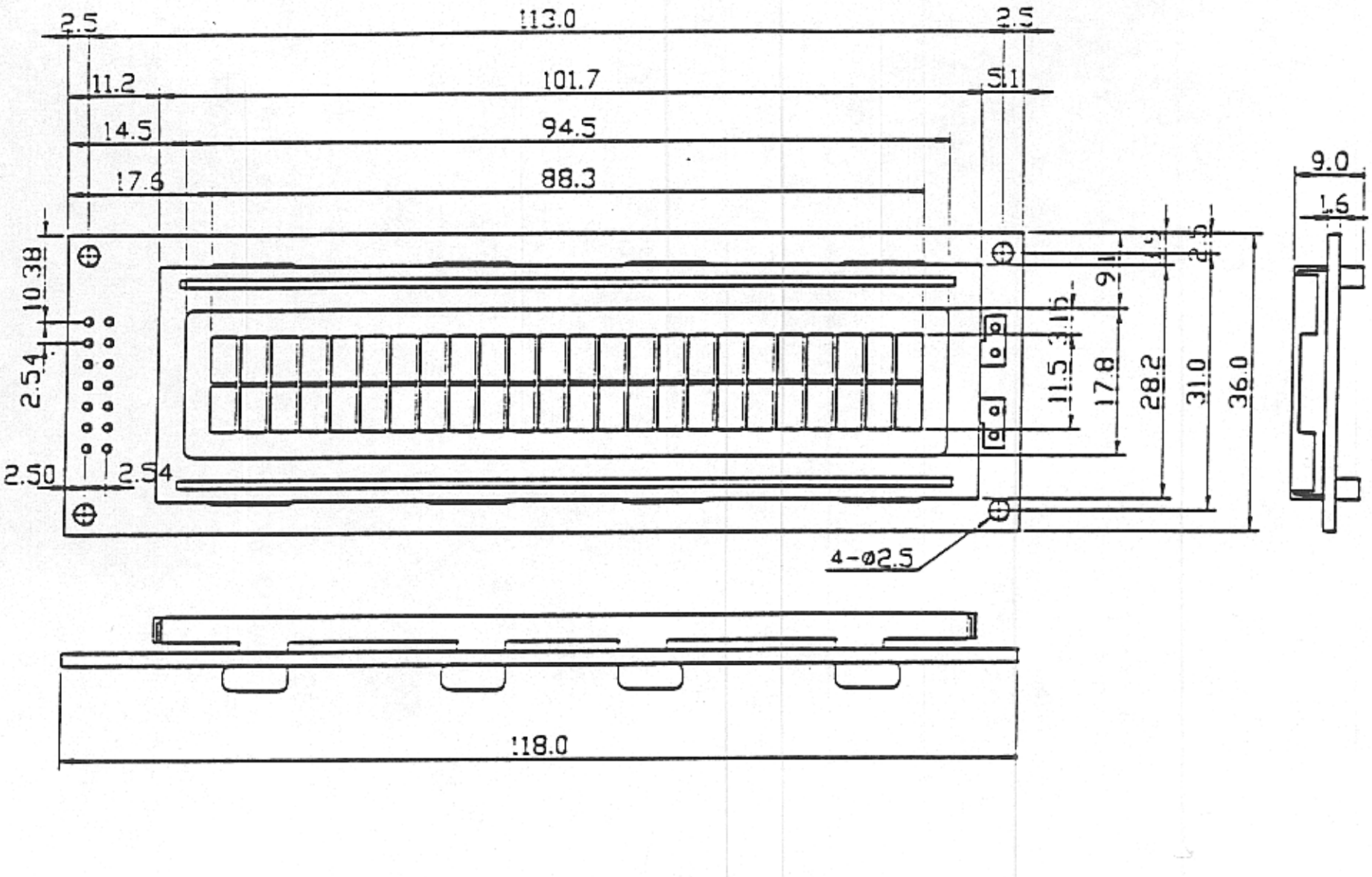
Items	Nominal Dimension	Unit
Character×Line	24×2	—
Module Size (W×H×T)	118.0×36.0×9.0	mm.
Viewing Area (W×H)	94.5×17.8	mm.
Active Area (W×H)	88.3×11.5	mm.
Character Font (W×H)	5×7 dots with cursor	—
Character Size (W×H)	3.20×5.55	mm.
Character Pitch (W×H)	3.70×5.95	mm.
Dot Size (W×H)	0.60×0.65	mm
Dot Pitch (W×H)	0.65×0.70	mm
Driving Method	1/32	Duty
Driving IC Assembly	COB	—

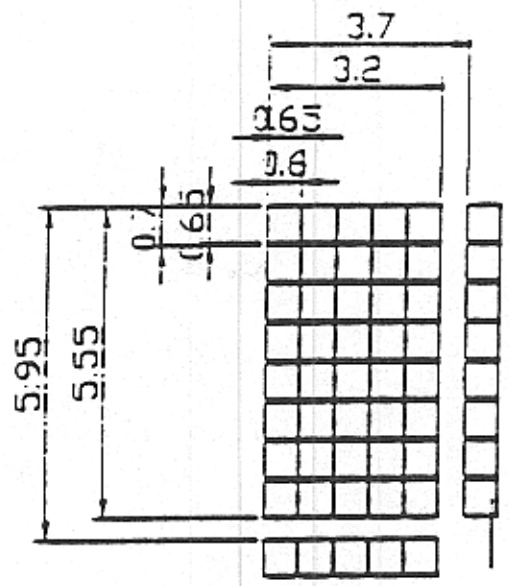
1-2 Display Specifications:

Display	Descriptions	Note
LCD Type	STN - Gray	
LCD Mode	Reflective	
Polarizer Mode	Positive	
Polarizer UV - Cutting	With	
Polarizer Surface	Normal	
Background Color	Yellow-Green	
Backlight Type	None	
Viewing Angle	6 O'clock Direction	

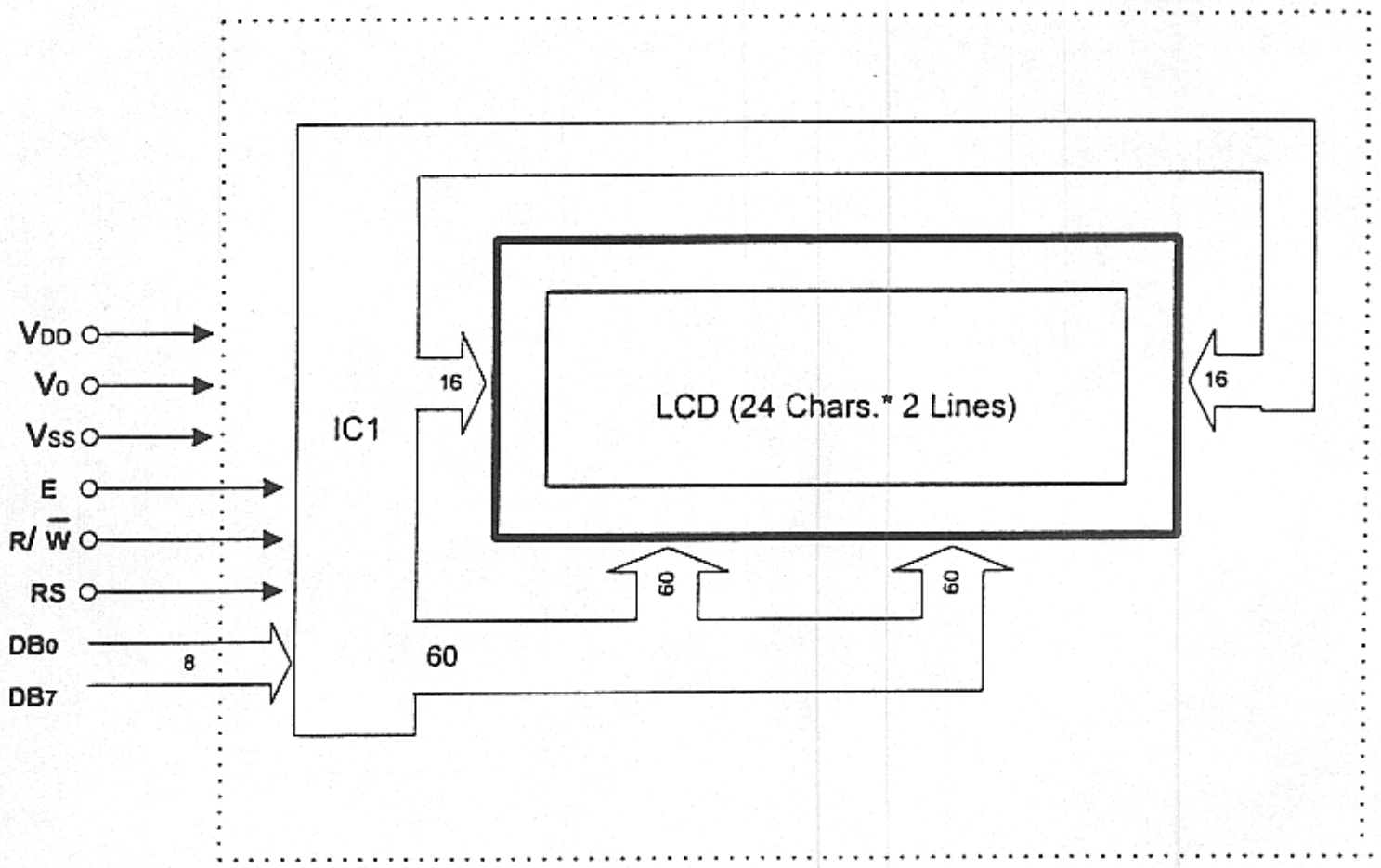
＊Color tone is slightly changed by temperature and driving voltage.

1-3 Outline Dimension

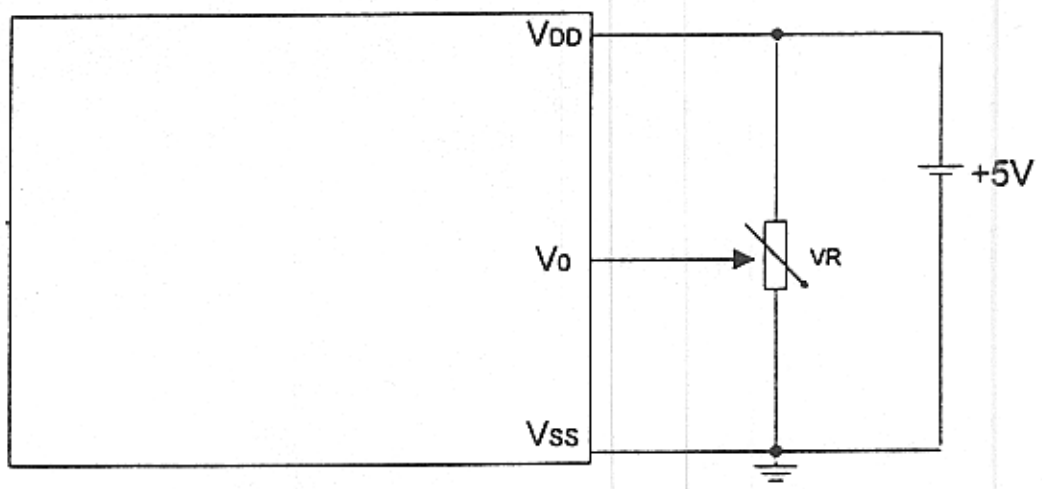




1-4 Block Diagram



☐ **Recommended Circuit for Contrast Adjustment**



1-5 Interface Pin Connection:

☐ Pin Table

Pin No.	Pin Symbol	Input/Ouput	Name	Description
1	V_{SS}		Power Supply	0V(GND)
2	V_{DD}		Power Supply	For logical circuit (+5V±10%)
3	V_O		Power Supply	Contrast adjustment voltage
4	RS	Input	Register select	Register selection input High:data register(when reading and writing) Low:instruction register(when writing) Busy flag and address counter (when reading)
5	R/$\overline{W}$	Input	Read/Write	R/W: signal is used to select the read/write mode High:reading Low:writing
6	E	Input	Enable	Start enable signal to read or write the data
7-14	DB0-DB7	Input/Ouput	Data Interface	Used for data transfer between the MPU and the controller. These terminals are for data bus with bidirectional three-state.Initial 4 bit(DB0-DB3) are not used during 4 bit operation(DB7 can be used as 1 busy flag)

2. Electrical Characteristics

2.1 Absolute Maximum Ratings

(V_{SS} = 0V, at 25℃)

Items	Symbol	Min.	Max.	Unit
Supply voltage for logics	V_{DD}-V_{SS}	-0.3	7.0	V
Supply voltage for driving LCD	V_{DD}-V_0	-0.3	13.5	V
Input voltage	V_{IN}	-0.3	V_{DD} +0.3	V
Operating temperature range	T_{OP}	0	40	℃
Storage temperature range	T_{ST}	-10	50	℃

2.2 DC Characteristics

(V_{DD} = 5V ± 10%, V_{SS} = 0V, at 25℃)

Items	Symbol	Min.	Typ.	Max.	Unit	Condition
Supply voltage (Logic)	V_{DD}-V_{SS}	4.5	5.0	5.5	V	
Supply voltage (LCD)	V_{DD}-V_0	3.5	3.9	4.5	V	*NOTE1
Input high level voltage	V_{IH}	2.3	--	V_{DD}	V	
Input low level voltage	V_{IL}	--	--	0.8	V	
Output high level voltage	V_{OH}	2.4	--	--	V	I_{OH}=-0.205mA
Output low level voltage	V_{OL}	--	--	0.4	V	I_{OL}=1.3mA
Power supply current	I_{DD}	--	--	3.0	mA	f_{osc}=270kHz *NOTE2

* The above spec. may be changed by Rev. No.

*NOTE1 : Min. and Max. Voltage is specified as the voltage within the condition of operational temperature range 0℃ ~ 40℃.

Typ.Voltage is specified as module driving condition: T_a=25℃, V_{OP} at Optimum Contrast.

*NOTE2 :
Measureing Condition:
Standard Value MAX.

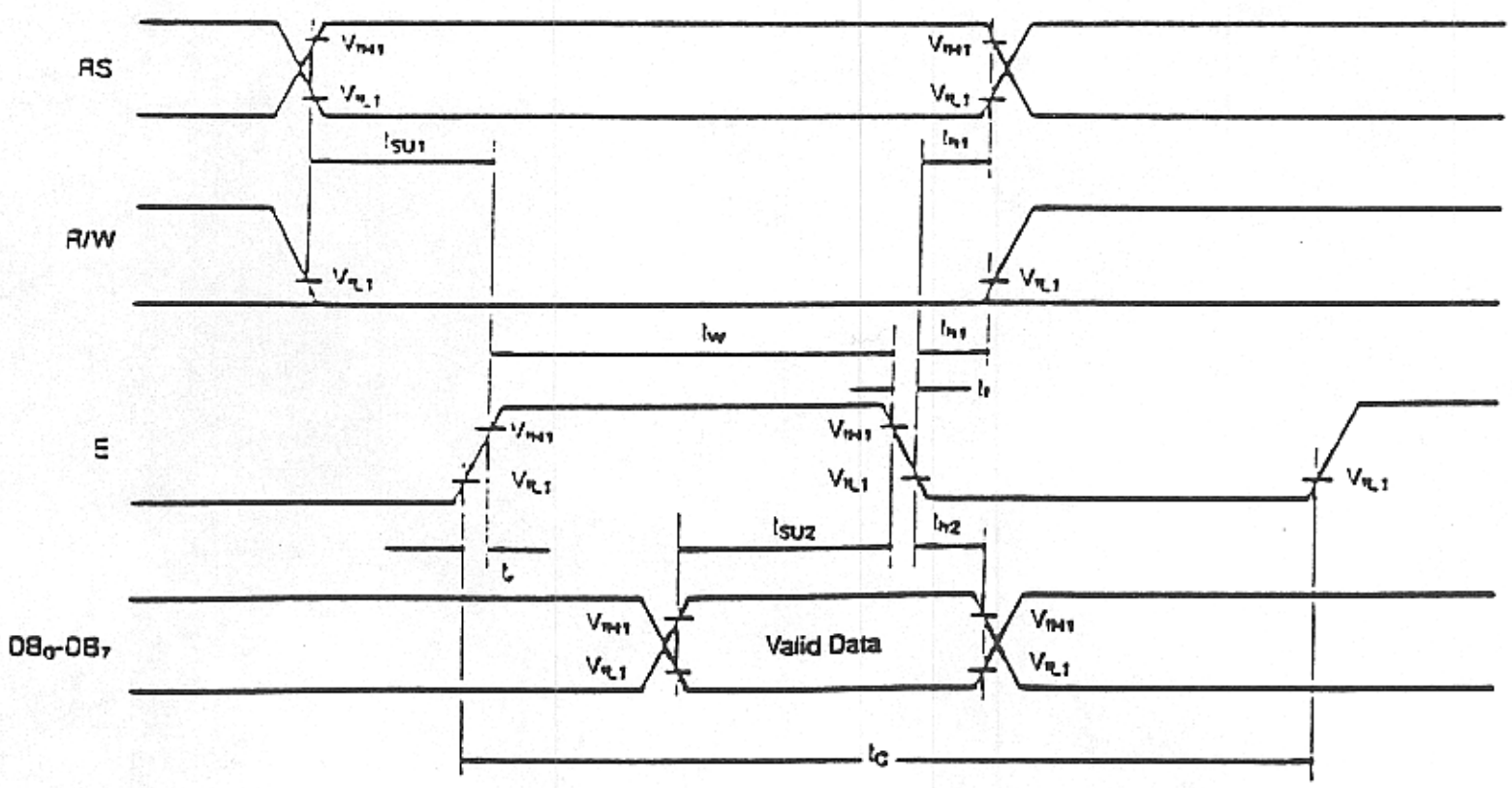
T_a	= 25℃
V_{DD} - V_{SS}	= 5.0V
V_{DD} - V_0	= V_{OP} at optimum Contrast
f_{osc}	= 270kHz
Duty	= 1/32 Duty
Display Pattern	= Checkered pattern

2.3 AC Characteristics

(1)Write Mode(Writing data from micro-computer to Controller)

Items	Symbol	Min.	Typ.	Max.	Test Pin	Uint
E cycle time	tc	500	-	-	E	ns
E rise time	t_r	-	-	20	E	ns
E fall time	t_f	-	-	20	E	ns
E pulse width(High,Low)	t_w	220	-	-	E	ns
R/W and RS set-up time	t_{su1}	40	-	-	R/W,RS	ns
R/W and RS hold time	t_{h1}	10	-	-	R/W,RS	ns
Data set-up time	t_{su2}	60	-	-	$DB_0 \sim DB_7$	ns
Data hold time	t_{h2}	10	-	-	$DB_0 \sim DB_7$	ns

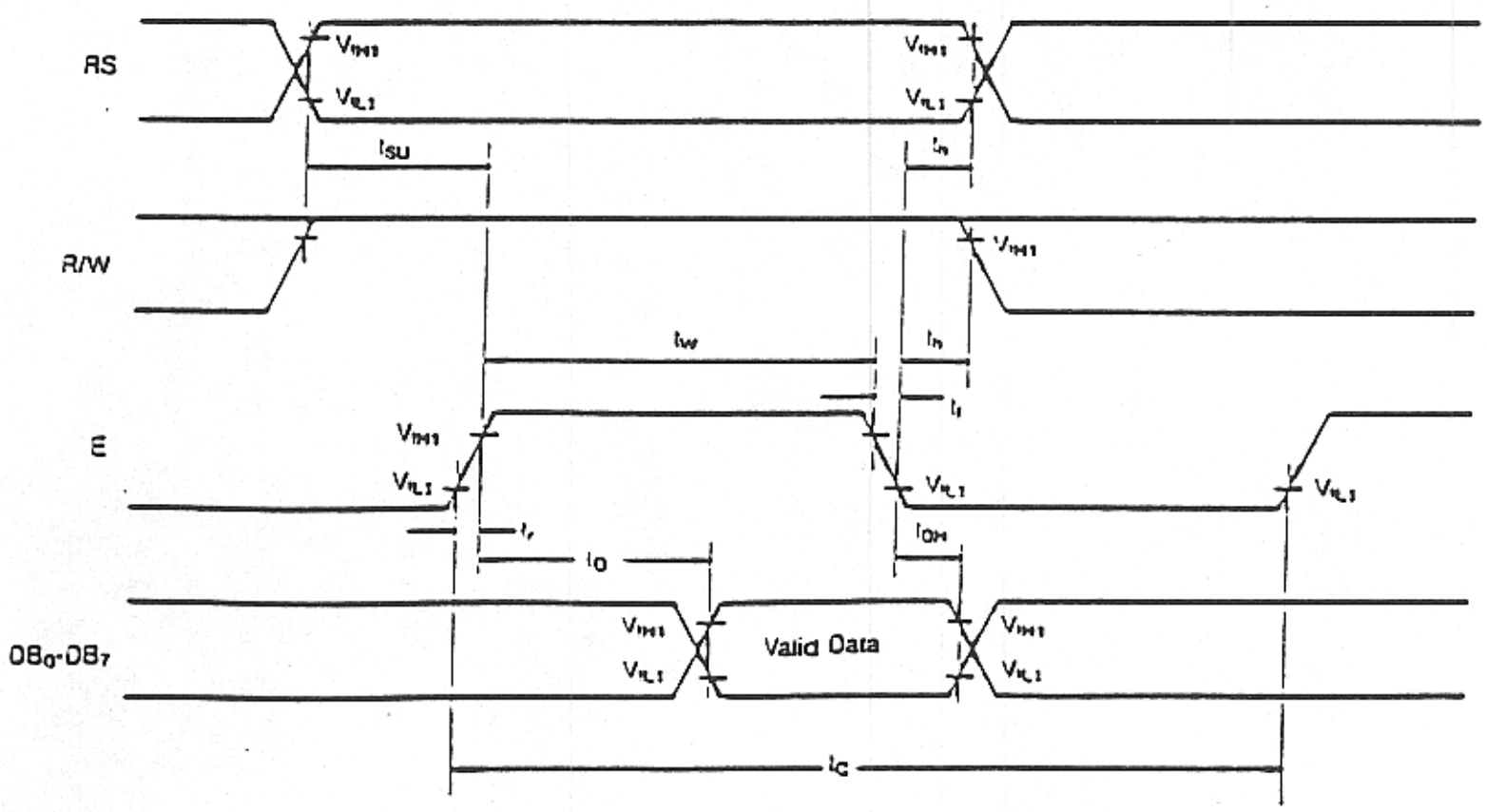
□Timing Characteristics



(2)Read Mode(Reading data from Controller to Micro-computer)

Items	Symbol	Min.	Typ.	Max.	Test Pin	Uint
E cycle time	tc	500	-	-	E	ns
E rise time	t_r	-	-	20	E	ns
E fall time	t_f	-	-	20	E	ns
E pulse width(High,Low)	t_w	220	-	-	E	ns
R/W and RS set-up time	t_{su}	40	-	-	R/W,RS	ns
R/W and RS hold time	t_h	10	-	-	R/W,RS	ns
Data set-up time	t_D	-	-	120	$DB_0 \sim DB_7$	ns
Data hold time	t_{DH}	20	-	-	$DB_0 \sim DB_7$	ns

□Timing Characteristics



3. Optical Characteristics

3.1 Optical Characteristics

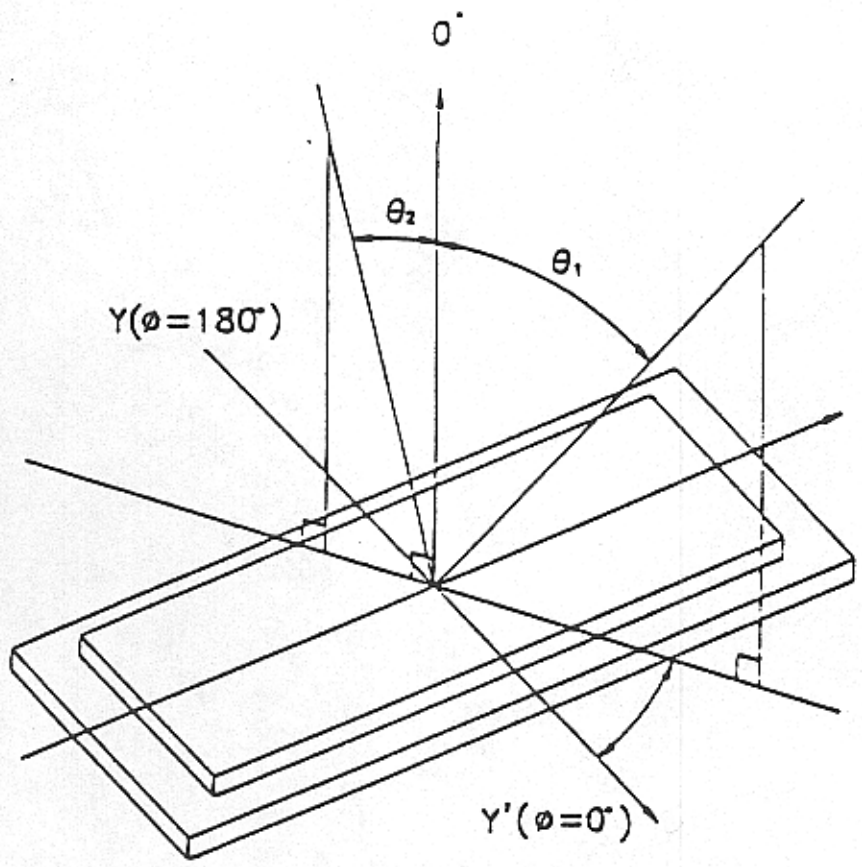
☐ STN

(V_{DD}=5V±5%)

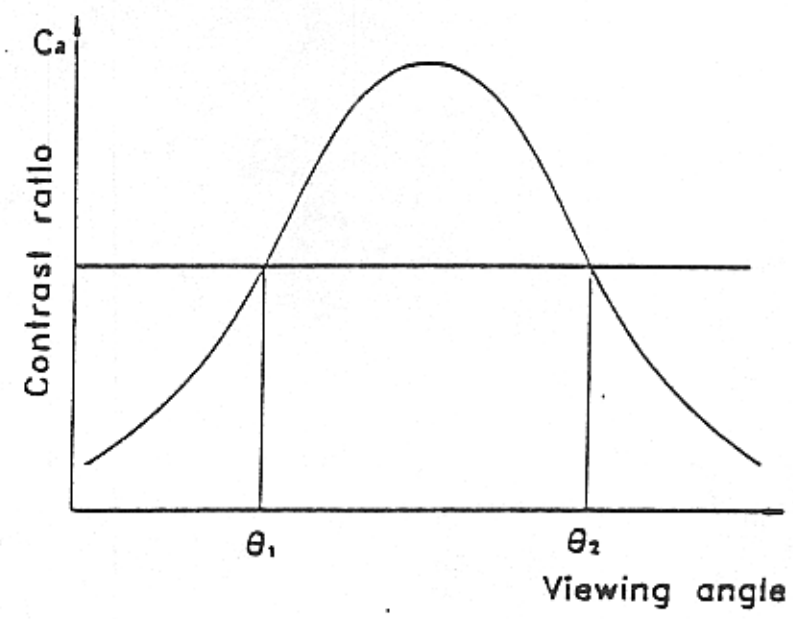
Items	Symbol	Condition	Min.	Typ.	Max.	Unit.
Response time	Rise time(t_r)	0℃	—	740	1480	ms
		25℃	—	180	360	ms
	Fall time(t_d)	0℃	—	290	580	ms
		25℃	—	175	350	ms
Viewing Angle(θ) Ta=25℃	Front-rear	ϕ=0°	40	—	—	degree
		ϕ=180°	0	—	—	degree
	Right-left	ϕ=90°	30	—	—	degree
		ϕ=270°	30	—	—	degree
Contrast ratio Ta=25℃	C_R	θ=0° ϕ=0°	4	—	—	

3.2 Definition of Optical Characteristics

Note 1) Definition of viewing direction θ and ø

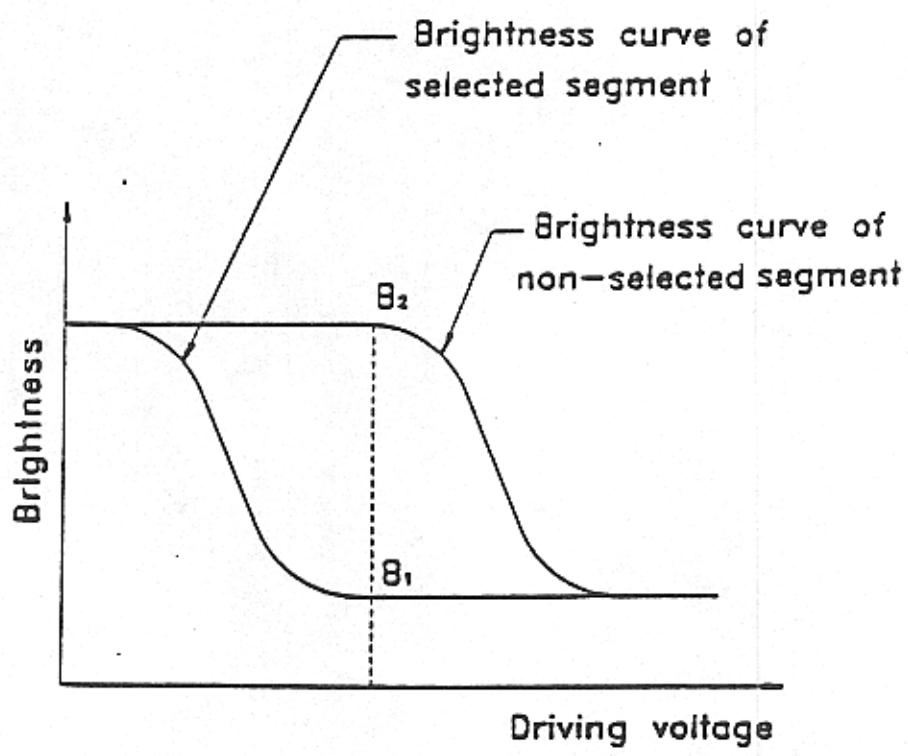


NOte 2) Definition of viewing angle θ_1 and θ_2

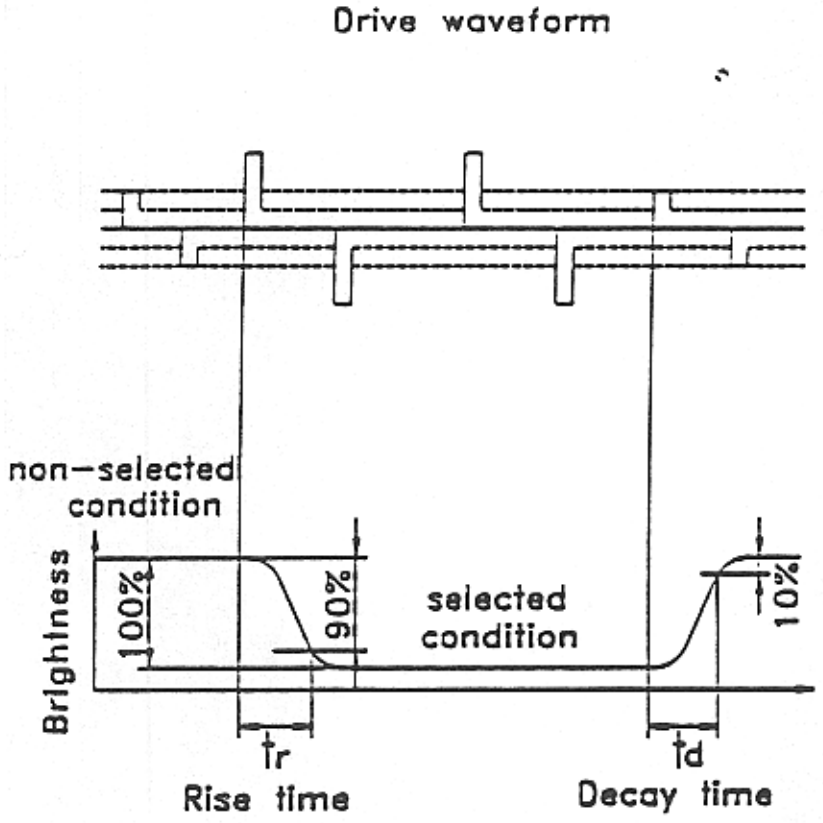


Note 3) Definition of contrast ratio C_R

$$C_R = \frac{\text{Brightness in non-selected segment}(B_2)}{\text{Brightness in selected segment}(B_1)}$$



Note 4) definition of optical response time



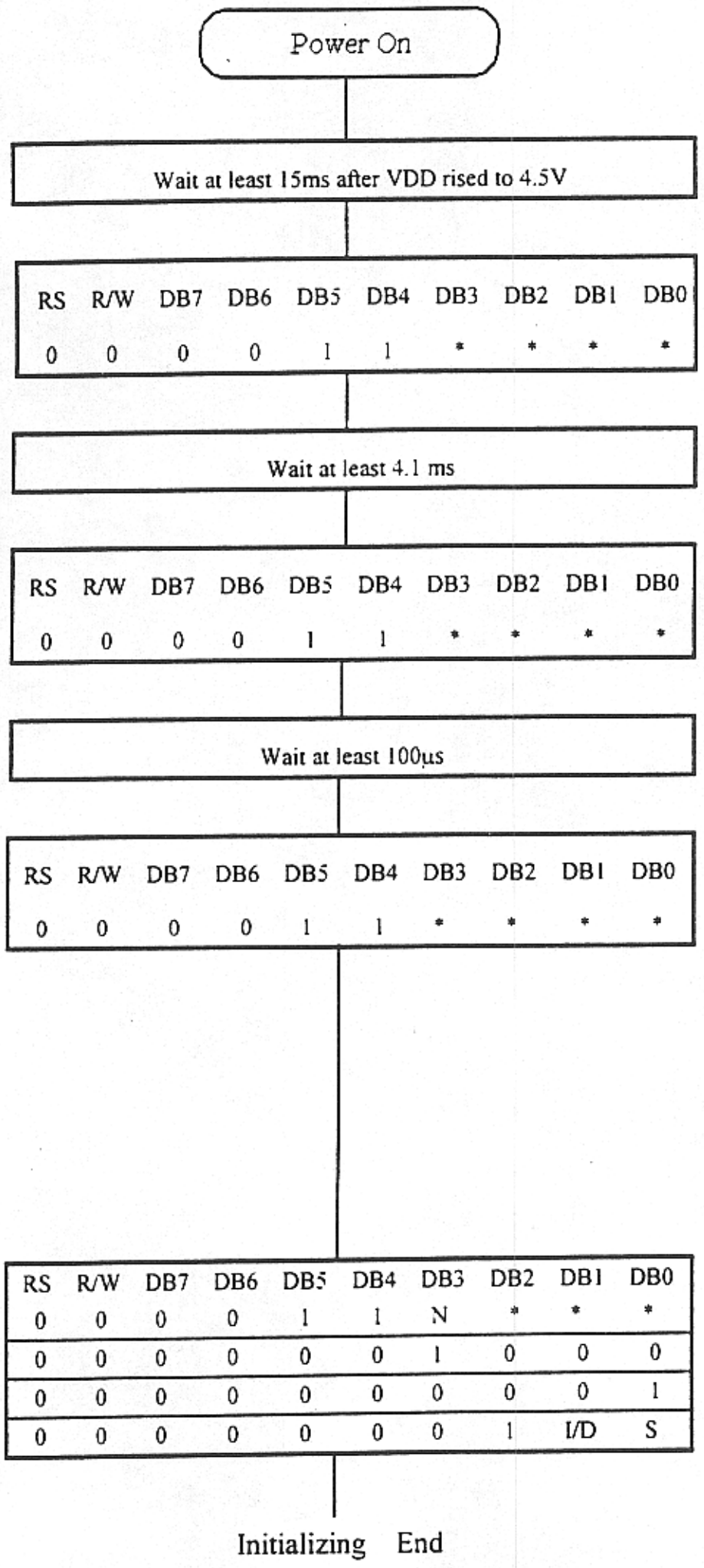
4. Control and Display Command

4.1 Instruction Table

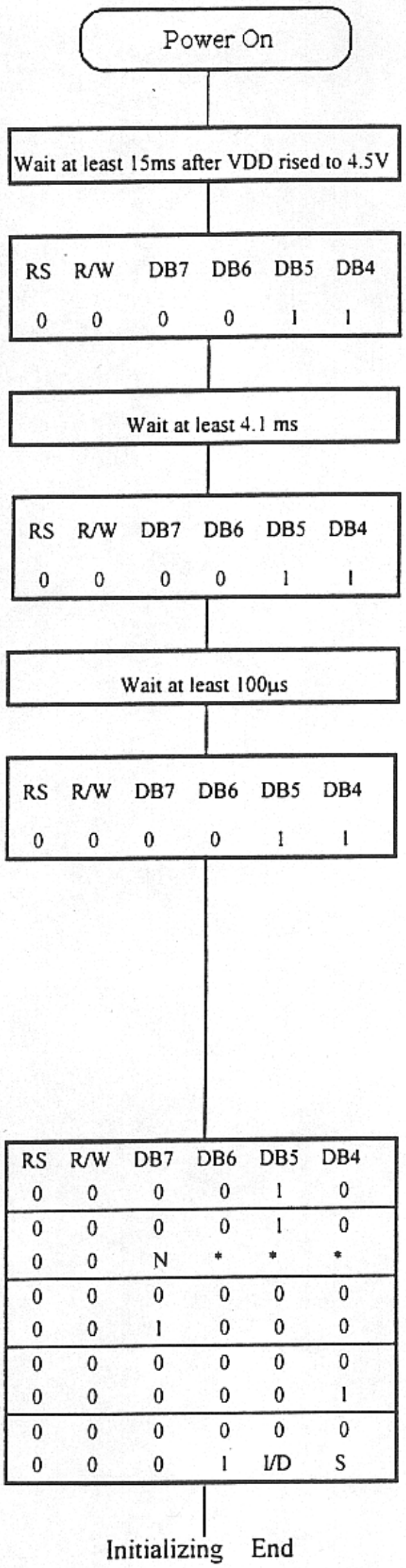
Instruction	Code										Description	Execution time (at fosc=250kHz)
	RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0		
Clear display	0	0	0	0	0	0	0	0	0	1	Clears entire display and sets DD - RAM address 0 in address counter	1.64ms
Cursor home	0	0	0	0	0	0	0	0	1	*	cursor move to the first digit	1.64ms
Entry mode set	0	0	0	0	0	0	0	1	I/D	SH	I/D: set cursor move direction H: increse L: decrese SH: Specifies shift of display H: display is shifted L: display is not shifted	40 µs
Display ON/OFF control	0	0	0	0	0	0	1	D	C	B	#Display H: display ON , L: display OFF #Cursor H: cursor ON. L: cursor OFF #Blinking H: blinking ON. L: blinking OFF	40 µs
Cursor/Display shift	0	0	0	0	0	1	S/C	R/L	.*	.*	S/C H: display shift L: cursor move R/L H: right shift L: left shift	40 µs
Function set	0	0	0	0	1	DL	N	*	.*	*.	DL-H: 8 bits interface -L: 4 bits interface N- H: 2 line display -L: 1 line display	40 µs
Set CGRAM address	0	0	0	1	CG RAM address (corresponds to cursor address)						CG RAM data is sent and received after this setting.	40 µs
Set DDRAM address	0	0	1	DDRAM address							DD RAM data is sent and received after this setting.	40 µs
Read busy flag/address	0	1	BF	Address counter used for both DD&CG RAM address							BF H: internally operating L: can accept instruction Reads BF indicating internal operation is being performed. and reads address counter contents.	0µs
Write data to CG or DD RAM	1	0	Write data								Writes data to DD RAM or CG RAM	46 µs
Read data to CG or DD RAM	1	1	Read data								Reads data to DD RAM or CG RAM	46 µs
	*:No Effect										DD RAM: Display data RAM CG RAM: Character generator RAM	Execution time changes with internal oscillation frequency (fosc) (Example) When fosc=270kHz 40µs × (250/270)=37µs

4.2 Initializing By Instruction

☐ **Initializing By Instruction (when interface is 8 bits long)**


Power On
Wait at least 15ms after VDD rised to 4.5V
RS R/W DB7 DB6 DB5 DB4 DB3 DB2 DB1 DB0
0 0 0 0 1 1 * * * *
Busy flag cannot be checked before this instruction is excuted.
Function Set (interface is 8 bits long)
Wait at least 4.1 ms
RS R/W DB7 DB6 DB5 DB4 DB3 DB2 DB1 DB0
0 0 0 0 1 1 * * * *
Busy flag cannot be checked before this instruction is executed.Function Set (interface is 8 bits long)
Wait at least 100μs
RS R/W DB7 DB6 DB5 DB4 DB3 DB2 DB1 DB0
0 0 0 0 1 1 * * * *
Busy flag cannot be checked before this instruction is executed. Function Set (interface is 8 bits long)
Busy flag can be checked after the following instructions.
When busy flag is not checked, the waiting time between instructions is longer than the execution time.
RS R/W DB7 DB6 DB5 DB4 DB3 DB2 DB1 DB0
0 0 0 0 1 1 N * * *
0 0 0 0 0 0 1 0 0 0
0 0 0 0 0 0 0 0 0 1
0 0 0 0 0 0 0 1 I/D S
Function Set (Interface is 8 bits long. Specify the number of display lines and character font.)
The number of display lines and character font cannot be changed afterwards.
=> Display Off
=> Display Clear
=> Entry Mode Set
Initializing End

☐ Initializing By Instruction (when interface is 4bits long)


Power On
Wait at least 15ms after VDD rised to 4.5V
RS R/W DB7 DB6 DB5 DB4
0 0 0 0 1 1
Busy flag cannot be checked before this instruction is excuted.
Function Set (interface is 8 bits long)
Wait at least 4.1 ms
RS R/W DB7 DB6 DB5 DB4
0 0 0 0 1 1
Busy flag cannot be checked before this instruction is executed.
Function Set (interface is 8 bits long)
Wait at least 100μs
RS R/W DB7 DB6 DB5 DB4
0 0 0 0 1 1
Busy flag cannot be checked before this instruction is executed.
Function Set (interface is 8 bits long)
Busy flag can be checked after the following instructions.
When busy flag is not checked. the waiting time between instructions is longer than the execution time.
RS R/W DB7 DB6 DB5 DB4
0 0 0 0 1 0
0 0 0 0 1 0
0 0 N * * *
0 0 0 0 0 0
0 0 1 0 0 0
0 0 0 0 0 0
0 0 0 0 0 1
0 0 0 0 0 0
0 0 0 1 I/D S
Function Set (Interface is 4 bits long. Specify the number of display lines and character font.)
The number of display lines and character font cannot be changed afterwards.
=> Display Off
=> Display Clear
=> Entry Mode Set
Initializing End

4.3 Standard Character Pattern

Upper 4 bit / Lower 4 bit	LLLL	LLHL	LLHH	LHLL	LHLH	LHHL	LHHH	HLLL	HLLH	HLHL	HLHH	HHLL	HHLH	HHHL	HHHH
LLLL	CG RAM (1)														
LLLH	(2)														
LLHL	(3)														
LLHH	(4)														
LHLL	(5)														
LHLH	(6)														
LHHL	(7)														
LHHH	(8)														
HLLL	(1)														
HLLH	(2)														
HLHL	(3)														
HLHH	(4)														
HHLL	(5)														
HHLH	(6)														
HHHL	(7)														
HHHH	(8)														

5. Reliability

5.1 Environmental Test

Item No	Items	Content of Test	Test Condition	Applicable Standard
1	High temperature storage	Endurance test applying the high storage temperature for a long time.	60℃ 200 Hrs	
2	Low temperature storage	Endurance test applying the low storage temperature for a long time.	-20℃ 200Hrs	
3	High temperature operation	Endurance test applying the electric stress (Voltage & Current) and the thermal stress to the element for a long time	40℃ 200Hrs (*1)	
4	Low temperature operation	Endurance test applying the electric stress under low temperature for a long time.	0℃ 200Hrs (*1)	
5	High temperature / humidity storage	Endurance test applying the high temperature and high humidity storage for a long time.	40℃ 90% RH 200Hrs	
6	Temperature cycle	Endurance test applying the low and high temperature cycles. -20℃ ⟷ 60℃ (30min.) (30min.) ⟷ 1 Cycle	10 Cycle.	
7	Vibration test	10 ⟶ 55 ⟶ 10 Hz, within 1 minute amplitude 1.5mm .	15 minutes for each direction(X,Y,Z)	
8	Drop test	Packed, 100CM free fall, (6 sides, 1corner, 3edges)		

***1) : Driving condition for operation test:**

Power supply voltage for logic system =+5V

Power supply voltage for LCD system = Getting Optimum Contrast at 25℃

6. HANDLING INSTRUCTION

☐ PRECAUTION IN USE OF LCD

- Do not contact or scratch the front surface and the contact pads of an LCD panel with hard materials such as metal or glass or with one's nail.
- To clean the surface,wipe it gently with soft cloth dampened alcohol.
- Do not attempt to wiped off the contact pads.
- Keep LCD panels away from direct sunlight,also avoid storing them in a high-temperature&high humidity environment for a long period.
- Do not drive LCD panels by DC voltage.
- Do not expose LCD panels to organic solvent.
- Liquid in LCD is hazardous substance, any contacts with liquid crystal materials ,wash it off immediately with soap and water.
- The polarizer is easily damaged and should be handle with special care.Do'nt press or rub it with hard objects.

☐ PRECAUTION FOR HANDLING LCM

- The LCD module contains a C-MOS LSI.To avoid damage to the LSI from static electricity generated while working,Ground your body,work/assembly areas and assembly equipment to protect the module against STATIC ELECTRICITY.
- Do not input any signal before power is turned on.
- Do not take LCM from it's packaging bag until it is assembled.
- Peel off the LCM protective film slowly since static electricity may be generated.
- Pay attention to the humidity of the work shop,50~60%RH is satisfactory.
- Use a non-leak iron for soldering LCM.
- Do not touch the display surface or connection terminals area with bare hands.Smudges on the display surface reduce the insulation between terminals.
- Cautions for soldering to LCM:
  Conditions for soldering I/O terminals:
  Temperature at iron tip: 280℃ ±10℃.
  Soldering time : 3~4 sec./ terminal.
  Type of solder : Eutectic solder(rosin flux filled).

☐ PRECAUTION FOR STORING LCM

- To avoid degradation of the device,do not store the module under the conditions of direct sunlight,high temperature or high humidity.Keep the module in bags designed to prevent static electricity charging under low temperature / normal humidity conditions(avoid high temperature / high humidity and low temperature below 0 ℃).

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5	Low temperature operation	Endurance test applying the electric stress under low temperature for a long time.	0℃ 200Hrs (*1)	
6	High temperature / humidity operation	Endurance test applying the electric stress (Voltage & Current) and the thermal stress to the element for a long time	40℃ 90% RH 96Hrs (*1)	
7	Temperature cycle	Endurance test applying the low and high temperature cycles. -20℃ ↔ 25℃ ↔ 60℃ ↔ 25℃ 30min. 5min. 30min. 5min. 1 Cycle	-20℃/60℃ 10 Cycle.	

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- Do not drive LCD panels by DC voltage.
- Do not expose LCD panels to organic solvent.
- Liquid in LCD is hazardous substance, any contacts with liquid crystal materials ,wash it off immediately with soap and water.
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